Interfacing AT25XXX Serial EEPROMs with AT89CX051 Microcontrollers

Serial Peripheral Interface

Serial memory devices offer significant advantages over parallel devices in applications where lower data transfer rates are acceptable. In addition to requiring less board space, serial devices allow microcontroller I/O pins to be conserved. This is especially valuable when adding external memory to low-pin count microcontrollers such as the Atmel AT89C1051 and AT89C2051.

This application note presents a suite of software routines which may be incorporated into a user's application to allow AT89CX051 microcontrollers to read from and write to AT25XXX serial EEPROMs. All six AT25XXX device operations are supported: read memory, write memory, read status, write status, (set write protection levels), write enable and write disable. Routines are also provided to read from and write to memory utilizing the page mode of the AT25XXX. The software supports both 3-wire and 4-wire configurations and meets all AT25XXX family timing requirements when run on an AT89CX051 microcontroller with a 24-MHz clock.

Hardware

The AT25XXX may be connected to the AT89CX051 microcontroller in either a 3-wire (Figure 1) or 4-wire (Figure 2) configuration. In the 3-wire configuration, the EEPROM serial data in (SI) and serial data out (SO) pins are both connected to the same microcontroller I/O pin, thereby saving a pin. This is possible because the microcontroller I/O pins can be dynamically reprogrammed as input or output.

Software

Software for this application note may be downloaded from Atmel's Web Site or BBS. Consult the comment block at the beginning of the source code file for detailed information on features and operation.

Figure 1. 3-Wire Configuration
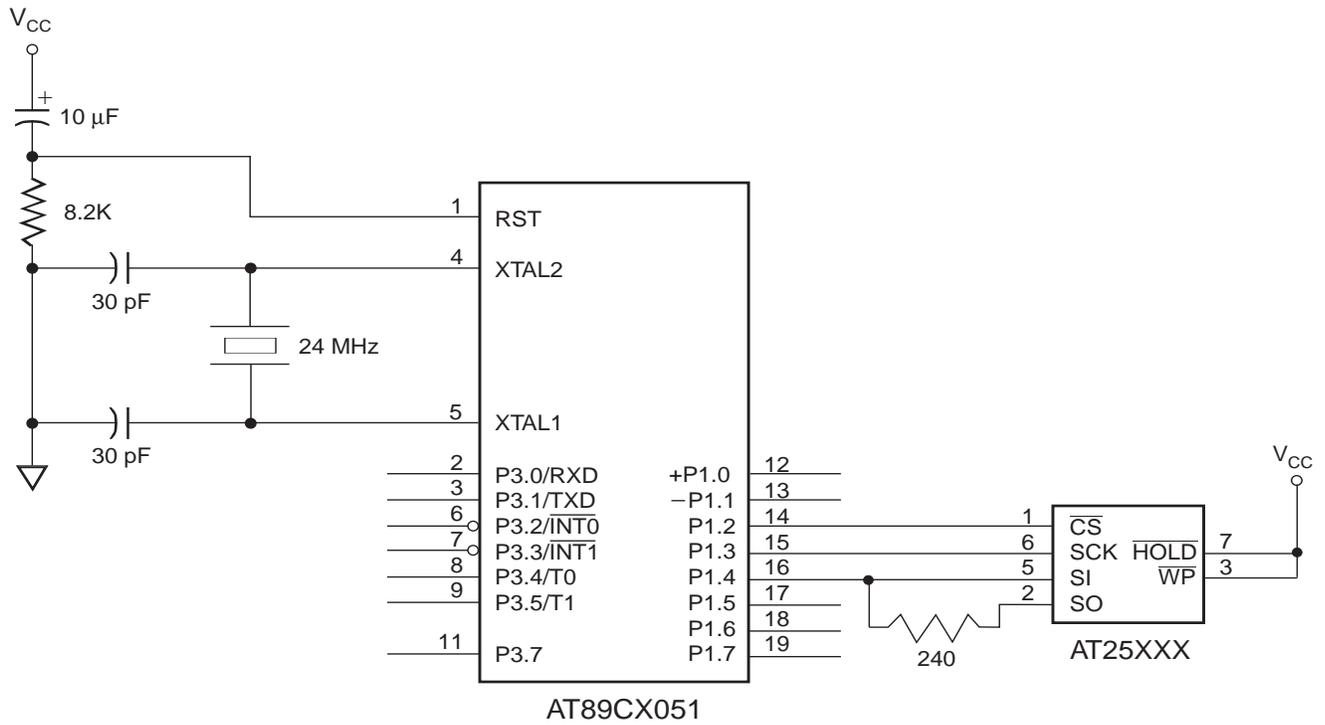
Figure 2. 4-Wire Configuration

Microcontroller