

## Multiplier Accumulator (MAC)

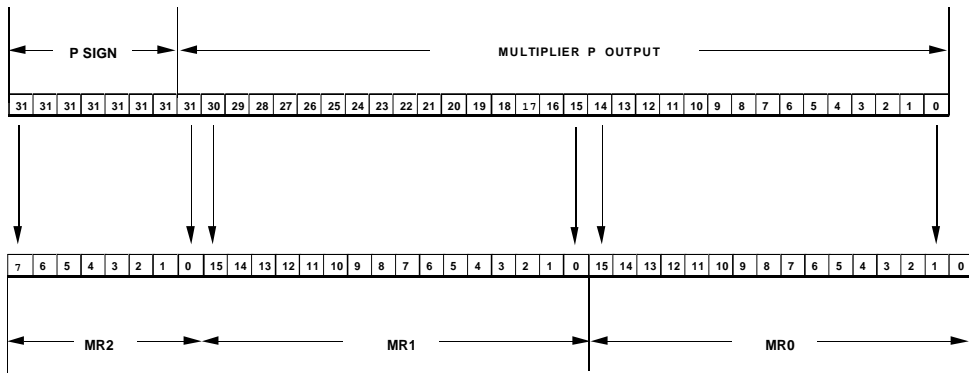


Figure 2-7. Fractional Multiplier Result Format

In the integer mode, the 32-bit P register is not shifted before being added to MR. [Figure 2-8](#) shows the integer-mode result placement.

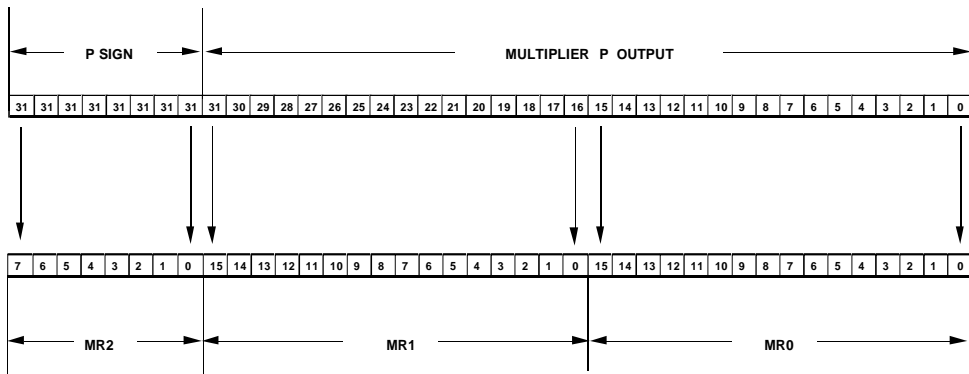


Figure 2-8. Integer Multiplier Results Format